

Amendments

In accordance with 37 CFR §1.121, please amend the above-identified application as set forth below.

Amendments to the Claims:

Please amend the claims as set forth below.

1.-7. (Cancelled)

8. (Currently Amended) A control processor for satellite broadcast of media content data comprising;

a control processor being configured to build control instruction commands, said control instruction commands being executable by an uplink for transmission of a digital video broadcast bitstream including control instructions contained within said control instruction command;

said control processor being in operative communication with a web server such that control instruction requests are received by said control processor after said requests are received by said web server in an HTTP transmission from a remote web browser;

said control processor being further configured to package control instructions from said control instruction requests in an email to at least one remote uplink, said control processor being further configured to send a control instruction command in response to an order remotely entered from said remote web browser; and

a communication link to a computer network, said communication link allowing said control instruction command to be emailed to remote uplinks;

wherein said remote uplink is not configured to receive control instruction requests at said remote uplink, said remote uplink only receiving control instruction commands through said emails from said control processor.

9. (Original) The control processor of the previous claim wherein said communication link further allows confirmation message from said at least one remote uplink back to said control processor via email.

10. (Currently Amended) A method of controlling a media content broadcast comprising:

receiving a control instruction request at a central processor from a remote input, through a computer network linked to both said central processor and said remote input; generating a control instruction command, said control instruction command being configured to be executable by an uplink for transmission of the control instructions to a plurality of remote receivers via satellite, said uplink being remote from said central processor; and

sending said control instruction command to the uplink through said computer network, said uplink also being linked to said computer network, said sending step being executed in response to a command from said remote input;

wherein said uplink is remote from said central processor and wherein said remote uplink is not configured to receive control instruction requests and wherein said remote uplink only receives control instruction commands through said email from said remote central processor.

11. (Previously Presented) The method of claim 10 wherein said computer network is the internet.
12. (Previously Presented) The method of claim 10 wherein said sending step is in batch mode.
13. (Previously Presented) The method of claim 10 wherein said sending step is in session mode.
14. (Previously Presented) The method of claim 10 wherein said control instruction command includes scheduling.
 15. (Cancelled)
 16. (Currently Amended) The system processor of claim [4] [8] wherein said control processor links to said computer network via a protocol selected from the group consisting of:
SMTMP, HTTP, FTP, and TFTFP.
17. (Currently Amended) The system processor of claim [4] [8] further comprising a graphical user interface with said control processor.
18. (Currently Amended) The system processor of claim [4] [8] wherein said control processor operates on Unix.
19. (Currently Amended) The system processor of claim [4] [8] wherein said link between said control processor and said computer network is an Ethernet/LAN link.
20. (Currently Amended) The system processor of claim [4] [8] wherein said control processor is associated with said web server via a socket server.
21. (Currently Amended) The system processor of claim [4] [8] further comprising a status memory in operative communication with said control processor.

22. (Currently Amended) The system processor of claim 21 wherein said status memory records a receiver status and user status.

23. (Currently Amended) The system processor of claim 21 further comprising an update driver, said update driver being configured to update said status memory to record a current status.

24. (Currently Amended) The system processor of claim [+] [8] further comprising a batch aggregator in operative communication with said control processor.

25. (Currently Amended) The system processor of claim 24 wherein said batch aggregator and said control processor are separate components.

26. (Currently Amended) The system processor of claim 24 wherein said batch aggregator is configured to complete a batch for transmission upon obtainment of a preconfigured batch volume.

27. (Currently Amended) The system processor of claim 24 wherein said batch aggregator is configured to complete a batch for transmission upon reaching a preconfigured time out.

28. (Currently Amended) The system processor of claim [+] [8] wherein said control processor and said web server communicate via a language selected from the group consisting of:

Perl, TCL, C, C++, or Visual Basic.

29. (Currently Amended) The system processor of claim [+] [8] wherein said uplink further comprises a control stream inserter.

30. (Currently Amended) The system processor of claim [+] [8] wherein said uplink further comprises a firewall.

31. (Currently Amended) The system processor of claim [4] [8] wherein said web server further comprises a firewall.

32. (Currently Amended) The system processor of claim [4] [8] wherein said uplink further comprises an encoder and a multiplexer.

33. (Currently Amended) The system processor of claim [4] [8] wherein said uplink further comprises an audiovisual input device.

34. (Currently Amended) The system processor of claim 33 wherein said audiovisual input device is a live feed.

35. (Currently Amended) The system processor of claim [4] [8] further comprising a schedule memory.

36. (Currently Amended) The system processor of claim 35 wherein said schedule memory is located at said uplink.

37. (Currently Amended) The system processor of claim 35 wherein said schedule memory is located at said control processor and in operative communication with said control processor.

38. (Currently Amended) The system processor of claim [4] [8] wherein said uplink is a conventional uplink, said conventional uplink further comprising a separate control processor.

39. (Currently Amended) The system processor of claim [4] [8] wherein said control instruction request includes a receiver address, a device address, a control parameter and a parameter data.

40. (Currently Amended) The system processor of claim [4] [8] further comprising default control instructions stored in a memory exit, said memory being operatively accessible by said control processor.

41. (Currently Amended) The system processor of claim [4] [8] further comprising an activity log.

42. (Currently Amended) The system processor of claim 41 wherein said activity log is searchable.

43. (Currently Amended) The system processor of claim [4] [8] wherein said control instruction request is encrypted.

44. (Currently Amended) The system processor of claim [4] [8] wherein said control instruction command is encrypted.

45. (Currently Amended) The system processor of claim [4] [8] wherein said control instruction command includes receipt confirmation instructions.

46. (Currently Amended) The system processor of claim [4] [8] wherein said control instruction command includes no-error confirmation instructions.

47. (Currently Amended) The system processor of claim 46 wherein said control processor is configured to resend a control instruction command if a no-error confirmation is not received.

48. (Currently Amended) The system processor of claim [4] [8] wherein said control processor is configured to update a status memory if a no-error confirmation message is received from said uplink.

49. (Currently Amended) The system processor of claim [+] [8] wherein said control instruction request includes an instruction to schedule transmission of control instructions at a later selectable time.

50. (Currently Amended) The system processor of claim [+] [8] wherein said control instruction command includes a control instruction packet.

51. (Currently Amended) The system processor of claim 50 wherein said control instruction packet includes a frame separator, a system identification, a length indicator, a sequence number, a remote address for an individual receiver, a class identifier, a device address, a command identifier, a command data value and a check sum.

52. (Currently Amended) The system processor of claim [+] [8] wherein said control instruction request includes a control instruction packet.

53. (Currently Amended) The system processor of claim 52 wherein said control instruction packet includes a frame separator, a system identification, a length indicator, a sequence number, a remote address for an individual receiver, a class identifier, a device address, a command identifier, a command data value and a check sum.

54. – 66. (Cancelled)

67. (Currently Amended) The system processor of claim [+] [8] wherein said uplink is operative to transmit data over a broadcast network to a plurality of receivers.

68. (Currently Amended) The system processor of claim [+] [8] wherein said communication link is remote from said control processor.

69. (Currently Amended) The system processor of claim [4] [8] wherein said communication link is remote from said uplink.

70. (Currently Amended) The system processor of claim [4] [8] wherein said communication link is remote from any of a plurality of receivers receiving said control transmissions.

71. (Currently Amended) The system processor of claim [4] [8] wherein said communication link is remote from said control processor, from said uplink and remote from any of a plurality of receivers receiving said control transmissions.

72. (Currently Amended) The system processor of claim [4] [8] having at least two uplinks.

73. (Currently Amended) The system processor of claim [4] [8] wherein said control instruction request is received by said control processor from said web server through said communication link.

74. (New) The control processor of claim 8 wherein said master control processor is configured to combine control instructions in said control instruction request with control instructions stored in a memory, said stored instructions being scheduled control instructions and wherein said master control processor is further configured to output an email combining said control instruction requests with said scheduled control instructions from memory in a single control instruction command.

75. (New) The control processor of claim 8 being further configured to receive control instruction requests entered into a master control web server by a subscriber to the media content.

76. (New) The control processor of claim 8 further configured to record a history of control instructions in a memory.

77. (New) The method of claim 10 wherein said master control processor is configured to combine control instructions in said control instruction request with control instructions stored in a memory, said stored instructions being scheduled control instructions and wherein said master control processor is further configured to output an email combining said control instruction requests with said scheduled control instructions from memory in a single control instruction command.

78. (New) The method of claim 10 being further configured to receive control instruction requests entered into a master control web server by a subscriber to the media content.

79. (New) The method of claim 10 further configured to record a history of control instructions in a memory.